

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Patent Application of:

Satoshi YAMANAKA et al.

Application No.: 10/541,611

Confirmation No.: 8340

Filed: July 07, 2005

Art Unit: 2625

For: PIXEL INTEPOLATION CIRCUIT, PIXEL  
INTERPOLATION METHOD AND IMAGE  
SCANNER

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Examiner: Zhu, Richard Z.

**AMENDMENT UNDER § 1.111**

**MS AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated May 20, 2010, the following amendments and remarks are respectfully submitted in connection with the above-identified application:

**Amendments to the Claims**

**Remarks**